Preface

Outsiders are not alone in finding the world of Industrial Ethernet somewhat confusing. Experts who examine the matter are similarly puzzled by a broad and intransparent line-up of competing systems. Most manufacturers provide very little information of that rare sort that captures technical characteristics and specific functionalities of a certain standard in a way that is both comprehensive and easy to comprehend. Users will find themselves even more out of luck if they are seeking material that clearly compares major systems to facilitate an objective assessment.

We too have seen repeated inquiries asking for a general overview of the major systems and wondering “where the differences actually lie”. We have therefore decided to dedicate an issue of the Industrial Ethernet Facts to this very topic. In creating this, we have tried to remain as objective as a player in this market can be. Our roundup focuses on technical and economic as well as on strategic criteria, all of which are relevant for a consideration of the long-term viability of investments in Industrial Ethernet equipment. The arguments made in this publication were advanced and substantiated in numerous conversations and discussions with developers and decision-makers in this field. We have made every attempt to verify claims whenever practically possible.

Despite all our efforts, though, we were unable to ascertain exact, verifiable information on some aspects, which prompts us to ask for your help: if you would like to propose any amendments or corrections, please send us an e-mail or simply give us a call. We look forward to any and all support in supplementing this overview, and we welcome all discussions that contribute to making the assessments of the various Industrial Ethernet standards as thorough and objective as possible. This second, expanded edition includes feedback submit by the Industrial Ethernet community after publication of the first issue in November, 2011.

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# Systems Roundup: The 5 Major Contenders

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This issue of Industrial Ethernet Facts compares PROFINET (RT, IRT), POWERLINK, EtherNet/IP, EtherCAT, and SERCOS III, i.e. five out of about 30 Industrial Ethernet systems currently in use around the world. Why these five? The selection was based on technical aspects, standardization status, and strategic market considerations. Relevant issues include e.g. whether a user organization backs the ongoing development of a protocol, whether a protocol is classified in the IEC standard, and whether a system is suitable for hard real-time requirements.

**Real-time**

A mechanism to resolve data collisions that is part of the IEEE 802.3 Ethernet standard causes irregular delays in data transfer. In order to achieve real-time performance, Industrial Ethernet protocols employ special preventive measures to avoid such collisions. For hard real-time, signal transmission times must stick exactly to a given time frame, or else they will trigger a failure signal. For soft real-time, some deviation within a limited span of time is tolerable. While cycle times of up to several hundred milliseconds may be good enough for soft real-time applications, e.g. for temperature monitoring, digital control systems or Motion Control applications often require cycle times below one millisecond.

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1 For a more extensive overview of systems, consult the list on www.pdv.reutlingen-university.de/rte/ compiled by Prof. Dr.-Ing. Jürgen Schwager, head of the Process Data Processing Lab at Reutlingen University.
Market Penetration

Another key aspect in selecting Industrial Ethernet systems for comparison was market penetration: various IMS and ARC surveys indicate that about three quarters of all Industrial Ethernet applications around the world use EtherNet/IP, PROFINET, or Modbus TCP. Next in line are POWERLINK and EtherCAT, two systems particularly suitable for hard real-time requirements. The following roundup does not examine Modbus TCP on its own, since its user organization ODVA has stated that it has been integrated into EtherNet/IP. SERCOS III, however, was included for comparison despite its marginal market share, because this system plays a vital role for fast Motion Control solutions.
Diverse Approaches to Real-time Generation
There are three different approaches to building a real-time Ethernet solution:
1. Based on TCP/IP: Protocols are based on standard TCP/IP layers with real-time mechanisms embedded in the top layer. These solutions usually have a limited performance range.
2. Standard Ethernet: Protocols are implemented on top of standard Ethernet layers. These solutions benefit from Ethernet evolution without further investment.
3. Modified Ethernet: The standard Ethernet layer, the Ethernet mechanism and infrastructure are modified. These solutions put performance before standard compliance.

One crucial difference of the various Industrial Ethernet systems compared within this publication lies in how they organize data transfer and how they manage to deliver real-time performance. EtherCAT and SERCOS III communicate using a summation frame method: in each cycle, data for all network nodes is sent in one telegram that travels from one node to another along the ring topology of the network, also collecting node responses on the way. In contrast to that, the single telegram procedure used by the other systems works by sending individual telegrams to the nodes, which also respond individually in separate telegrams.

The systems use three different mechanisms for network access and data synchronization:
- A master controls the timing on the network. In POWERLINK environments, the master authorizes individual nodes to send data. In EtherCAT and SERCOS III networks, the transfer of summation frame telegrams follows the master’s clock.
- PROFINET IRT uses synchronized switches to control communication.
- EtherNet/IP employs CIP Sync to distribute IEEE 1588 compliant time information throughout the network.
PROFINET (“Process Field Network”) is differentiated into different performance classes to address various timing requirements: PROFINET RT for soft real-time, or no real-time requirements at all, and PROFINET IRT for hard real-time performance. The technology was developed by Siemens and the member companies of the PROFIBUS user organization, PNO. The Ethernet-based successor to PROFIBUS DP, PROFINET I/O specifies all data transfer between I/O controllers as well as the parameterization, diagnostics, and layout of a network.

How It Works
In order to cover the different performance classes, PROFINET makes free use of the producer/consumer principle and resorts to various protocols and services. High-priority payload data sent directly via the Ethernet protocol travels in Ethernet frames with VLAN prioritization, whereas diagnostics and configuration data, for instance, is sent using UDP/IP. That enables the system to achieve cycle times of around 10 ms for I/O applications.

Clock-synchronized cycle times below one millisecond, as required for Motion Control applications, are provided by PROFINET IRT, which implements a time multiplex mode based on specially managed, hardware-synchronized switches. So-called Dynamic Frame Packing (DFP) will in the future give users a new PROFINET variant designed to optimize cycle times making use of the summation frame principle for a certain set of devices in the network.
POWERLINK Communication

Initially developed by B&R, POWERLINK was introduced in 2001. The Ethernet POWERLINK Standardization Group (EPSG), an independent user organization with a democratic charter, has taken charge of the further development of the technology since 2003. POWERLINK is a completely patent-free, vendor-independent and purely software-based communication system that delivers hard real-time performance. An open source version has also been made available free of charge in 2008. POWERLINK integrates the entire range of CANopen mechanisms and fully complies with the IEEE 802.3 Ethernet standard, i.e. the protocol provides all standard Ethernet features including cross-traffic and hot plugging capability, and allows for deploying any network topology of choice.

How It Works

POWERLINK uses a mixture of timeslot and polling procedures to achieve isochronous data transfer. In order to ensure co-ordination, a PLC or an Industrial PC is designated to be the so-called Managing Node (MN). This manager enforces the cycle timing that serves to synchronize all devices and controls cyclical data communication. All other devices operate as Controlled Nodes (CN). In the course of one clock cycle, the MN sends so-called “Poll Requests” to one CN after another in a fixed sequence. Every CN replies immediately to this request with a “Poll Response” on which all other nodes can listen in.

A POWERLINK cycle consists of three periods. During the “Start Period,” the MN sends a “Start of Cycle” (SoC) frame to all CNs to synchronize the devices. Jitter amounts to about 20 nanoseconds. Cyclic isochronous data exchange takes place during the second period (“Cyclic Period”). Multiplexing allows for optimized bandwidth use in this phase. The third period marks the start of the asynchronous phase, which enables the transfer of large, non-time-critical data packets. Such data, e.g. user data or TCP/IP frames, is scattered between the asynchronous phases of several cycles. POWERLINK distinguishes between real-time and non-real-time domains. Since data transfer in the asynchronous period supports standard IP frames, routers separate data safely and transparently from the real-time domains. POWERLINK is very well suited to all sorts of automation applications including I/O, Motion Control, robotics tasks, PLC-to-PLC communication and visualization.
EtherNet/IP Communication

Initially released in 2000, EtherNet/IP is an open industrial standard developed by Allen-Bradley (Rockwell Automation) and the ODVA (Open DeviceNet Vendors Association). The “Ethernet Industrial Protocol” is essentially a port of the CIP application protocol (Common Industrial Protocol), which was already used by ControlNet and DeviceNet, to the Ethernet data transfer protocol. EtherNet/IP is particularly well established on the American market and is often used with Rockwell control systems.

How It Works

EtherNet/IP runs on standard Ethernet hardware and uses both TCP/IP and UDP/IP for data transfer. Due to the producer/consumer functionality supported by the CIP protocol, EtherNet/IP has various communication mechanisms at its disposal, e.g. cyclic polling, time or event triggers, multicast or simple point-to-point connections. The CIP application protocol differentiates between “implicit” I/O messages and “explicit” query/reply telegrams for configuration and data acquisition. While explicit messages are embedded into TCP frames, real-time application data is sent via UDP due to the latter protocol’s more compact format and smaller overhead. Forming the center of a star topology network, switches prevent collisions of data from devices that are connected using point-to-point connections. EtherNet/IP typically achieves soft real-time performance with cycle times around 10 milliseconds. CIP Sync and CIP Motion as well as precise node synchronization via distributed clocks as specified in the IEEE 1588 standard are used to approach cycle times and jitter values low enough to enable servo motor control.
EtherCAT Communication

EtherCAT (“Ethernet for Control Automation Technology”) was developed by Beckhoff Automation. All users of this technology automatically become members of the EtherCAT Technology Group (ETG).

How It Works
EtherCAT is based on the summation frame method: The EtherCAT master transmits an Ethernet frame containing data for all nodes on the network. That frame passes through all nodes in sequence. When it arrives at the last node on a trunk, the frame is turned back again. The nodes process the information in the frame as it passes through in one direction. Each node reads out data addressed to it on the fly, and inserts response data back into the frame. In order to support the bandwidth of 100 Mbit/s, special hardware based on ASICs or FPGAs is required for fast processing as data passes through. In effect, the topology of an EtherCAT network always constitutes a logical ring. Even trunks branching out, which can be hooked up to nodes especially designed for such connections, actually only add a two-way junction where the summation frame telegram travels up and back down the branching line.

Structure of an EtherCAT frame
All EtherCAT telegrams with instructions for individual nodes are contained within the payload data area of a frame. Each EtherCAT frame consists of one header and several EtherCAT commands. Each of these comprises its own header, instruction data for a slave, and a working counter. Up to 64 Kbytes configurable address space is available for each slave. Addressing proceeds by auto-increment, i.e. each slave counts up the 16-bit address field. Slaves can also be addressed via distributed station addresses, which are assigned by the master in the start-up phase.

EtherCAT Process Synchronization
Every slave connection provides a real-time clock that is synchronized by the master using a technique similar to IEEE 1588. There are slave devices with and without real-time mechanisms, since these are more demanding on the hardware. Based on the real-time clocks, control signals can be synchronized with high precision. In physical terms, the EtherCAT protocol not only runs on Ethernet, but also on LVDS (Low Voltage Differential Signaling). This standard is used by Beckhoff as an internal bus on the terminals. A PC with a standard Ethernet interface is typically used to implement an EtherCAT master. In contrast to other protocols such as POWERLINK or PROFINET, EtherCAT solely extends to Layers 1 through 3 of the seven-layer OSI model. Hence, in order to achieve application functionality comparable to the other systems, an extra protocol layer (CoE, EoE) needs to be superimposed.
A freely available real-time communication standard for digital drive interfaces, SERCOS III not only specifies the hardware architecture of the physical connections but also a protocol structure and an extensive range of profile definitions. For SERCOS III, effectively the third generation of the Sercos Interface that was originally introduced to the market in 1985, Standard Ethernet according to IEEE 802.3 serves as the data transfer protocol. This communication system is predominantly used in Motion Control-based automation systems. A registered association, sercos International e.V., supports the technology’s ongoing development and ensures compliance with the standard.

**How It Works**

While specific hardware is categorically needed for the slave, a software solution is also feasible for the master. The sercos user organization provides a SERCOS III IP core to support FPGA-based SERCOS III hardware development. SERCOS III uses a summation frame method. Network nodes must be deployed in a daisy chain or a closed ring. Data is processed while passing through a device, using different types of telegrams for different communication types. Due to the full-duplex capability of the Ethernet connection, a daisy chain actually constitutes a single ring, whereas a proper ring topology will in effect provide a double ring, allowing for redundant data transfer. Direct cross-traffic is enabled by the two communication ports on every node: in a daisy chain as well as a ring network, the real-time telegrams pass through every node on their way back and forth, i.e. they are processed twice per cycle. Hence, devices are capable of communicating with each other within one communication cycle, with no need to route their data through the master.

Besides the real-time channel, which uses time slots with reserved bandwidths to ensure collision-free data transfer, SERCOS III also provides for an optional non-real-time channel. Nodes are synchronized on the hardware level, prompted by the first real-time telegram at the beginning of a communication cycle. The master Synchronization Telegram (MST) is embedded into the first telegram for that purpose. Ensuring high precision by keeping synchronization offsets below 100 nanoseconds, a hardware-based procedure compensates for runtime delays and variations in it resulting from the Ethernet hardware. Various network segments may use different cycle clocks and still achieve fully synchronized operation.
User independence is another key aspect in the overall assessment of a system. Any unsettled issues regarding brand rights or patents that may limit a user’s own developments are crucial factors to consider when making the decision for a system. Legal traps that may cause inconvenience later can be avoided by taking a close look at the creators and the user organizations backing the various solutions.

**PROFINET – PI**

PROFIBUS & PROFINET International (PI) is the international umbrella association for 25 regional PROFIBUS & PROFINET Associations including the user organization PNO, which spells out as PROFIBUS Nutzerorganisation e. V. It runs an office that manages joint projects and disperses information to members and other interested parties. A certification center for PROFIBUS and PROFINET product approvals is affiliated with that office. Adopted on 24 April, 1996, the organization’s bylaws specify its duties and responsibilities. Membership is open to all companies, associations, and institutions that support the interests of PI as device vendors, users, systems solution providers or operators of PROFIBUS or PROFINET networks. www.profibus.com

**POWERLINK – EPSG**

The Ethernet POWERLINK Standardization Group (EP SG) was founded in 2003 as an independent organization of companies in the drives and automation sector. The group’s goal is the standardization and ongoing development of the POWERLINK protocol introduced by B&R in 2001. The EPSG cooperates with standardization organizations such as CAN in Automation (CiA) or the IEC. The EPSG is a registered association established according to Swiss civil law. www.ethernet-powerlink.org

**EtherNet/IP – ODVA**

ODVA is the union of all DeviceNet and EtherNet/IP users. The organization attends to the continual development and further distribution of these field buses that are predominantly used in the USA and Asia, but also in Europe. One key aspect of the organization’s activities is the development and propagation of the CIP protocol and of other protocols based on it. Users may not only apply the technology but are also invited to contribute to its ongoing development by joining Special Interest Groups (SIG). The ODVA also actively participates in other standardization bodies and industry consortia. The organization’s bylaws are relatively complex. www.odva.org

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### Criteria PROFINET

<table>
<thead>
<tr>
<th>RT</th>
<th>POWERLINK</th>
<th>EtherNet/IP</th>
<th>EtherCAT</th>
<th>SERCOS III</th>
</tr>
</thead>
<tbody>
<tr>
<td>PNO</td>
<td>EPSG</td>
<td>ODVA</td>
<td>ETG</td>
<td>SERCOS International</td>
</tr>
</tbody>
</table>

www.profibus.com ethernet-powerlink.org odva.org ethercat.org sercos.org
EtherCAT – ETG
The EtherCAT Technology Group is a forum jointly established by users, OEMs, machine vendors, and other automation suppliers. The group’s purpose is to provide support for and to propagate the benefits of EtherCAT as an open technology. A certification lab is affiliated with the organization’s head office in Nuremberg. All contractual agreements for use of the technology must be made directly with Beckhoff Automation. Based in Nuremberg, Germany, the EtherCAT Technology Group is a “nicht eingetragener Verein”, i.e. a non-registered club in the sense of the German Civil Code.
www.ethercat.org

SERCOS III – sercos International e. V.
sercos International e.V. (SI) is an association entered into the court registry in Frankfurt am Main, Germany. The association’s members are manufacturer and users of control systems, drives, and other automation components as well as machine vendors, research institutions, and other associations. There are subsidiary organizations in North America and Asia. A certification lab at the University of Stuttgart is affiliated with the organization’s head office.
www.sercos.org
**Status, Rights and Licensing**

Which is the legal status of the various user organizations? Who owns the technology? Which legal ties, depending on the licensing regime, are binding for developers who use a specific technology? The following pages provide an overview.

**EtherCAT Technology Group:** the non-registered club is not a legal entity, but is effectively a hybrid between an association and a private partnership, for which legal liabilities remain unclear.

In most cases, the rights to a technology rest with the organization responsible for it. As co-owners, members are therefore entitled to make use of it. If other persons or companies own the rights to a technology, the prospects for future legal use of it remain unclear.
Membership in the ETG is free of charge. A fee is due for memberships in all other organizations. Annual contributions usually vary with the size of a corporate member. POWERLINK’s and sercos’ user organizations also allow non-members to develop products and put them on the market.

While communication mechanisms are specified for SERCOS III and EtherCAT, the inner workings of a slave remain undisclosed. Users must resort to an ASIC or an FPGA. FPGA IP code from Beckhoff is available for EtherCAT as object code, the source code is not disclosed.

PROFINET: The PROFIBUS user organization (PNO) provides source code and documentations for PROFINET implementations (PROFINET runtime software) to its members. Clause 1.5 of the license agreement for this software gives PNO members the right to use five patents.

POWERLINK: POWERLINK master and slave code is freely available under a BSD open source license. The software stack is available on SourceForge.net.

EtherNet/IP: Stacks are available for purchase from various service providers. An open source variant has been developed by a university.

EtherCAT: Slave implementations necessarily require an ASIC or an FPGA. The VHDL or IP code for the FPGA must be purchased from Beckhoff; no source code for it is available. The ETG provides sample source code for the master side. Since the patent holder has not agreed to an open source licensing regime, that source code does not qualify as open source.*

SERCOS III: Software master is provided free of charge under an LGPL license. ASICs or FPGA code must be purchased for the slave.

* Source: Open Source Automation Development Lab (www.osadl.org)
Investment Viability

Openness as one issue with a bearing on the long-term viability of investments in a system has already been mentioned. In addition, a number of technical and strategic considerations also play crucial roles in making a safe investment decision for the long term.

Compatibility to Existing Application Profiles

<table>
<thead>
<tr>
<th>Criteria</th>
<th>PROFINET RT</th>
<th>PROFINET IRT</th>
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<th>EtherCAT</th>
<th>SERCOS III</th>
</tr>
</thead>
<tbody>
<tr>
<td>Downward compatibility</td>
<td>+</td>
<td>+</td>
<td>+</td>
<td>+</td>
<td>+</td>
<td>+</td>
</tr>
</tbody>
</table>

EMC Susceptibility/Transmission Reliability

Summation frame protocols are more susceptible to interference than single frame protocols. If a frame is destroyed, summation frame protocols always lose an entire cycle.

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<tr>
<th>Criteria</th>
<th>PROFINET RT</th>
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<th>EtherCAT</th>
<th>SERCOS III</th>
</tr>
</thead>
<tbody>
<tr>
<td>EMC susceptibility</td>
<td>+</td>
<td>+</td>
<td>+</td>
<td>0</td>
<td>0</td>
<td></td>
</tr>
</tbody>
</table>

Since it uses two telegrams, in this comparison SERCOS III actually provides 50% better performance than EtherCAT.

Flexible Cabling Topology

EtherCAT and SERCOS III networks always constitute a logical ring. That ring can be physically closed at the master, or, in the case of a daisy chain, closed internally at the last node in the physical line. EtherCAT does provide for trunks to branch out via special junctions, but the entire frame travels up and back down such lateral network lines, i.e. the network as a whole still represents a logical ring.

<table>
<thead>
<tr>
<th>Criteria</th>
<th>PROFINET RT</th>
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<th>EtherCAT</th>
<th>SERCOS III</th>
</tr>
</thead>
<tbody>
<tr>
<td>Tree topology</td>
<td>+</td>
<td>+</td>
<td>+</td>
<td>0</td>
<td>0</td>
<td></td>
</tr>
<tr>
<td>Star topology</td>
<td>+</td>
<td>+</td>
<td>+</td>
<td>0</td>
<td>0</td>
<td></td>
</tr>
<tr>
<td>Ring topology</td>
<td>+</td>
<td>+</td>
<td>+</td>
<td>+</td>
<td>+</td>
<td></td>
</tr>
<tr>
<td>Daisy-chain topology</td>
<td>+</td>
<td>+</td>
<td>+</td>
<td>+</td>
<td>+</td>
<td></td>
</tr>
</tbody>
</table>

High Availability

Only in the case of POWERLINK have master and cable redundancy been included in the specifications, and have been implemented in actual projects. For PROFINET and EtherNet/IP, application implementations based on special switches are feasible.

<table>
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<tr>
<th>Criteria</th>
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<th>EtherCAT</th>
<th>SERCOS III</th>
</tr>
</thead>
<tbody>
<tr>
<td>Ring redundancy</td>
<td>0</td>
<td>+</td>
<td>0</td>
<td>+</td>
<td>+</td>
<td></td>
</tr>
<tr>
<td>Master and cable redundancy</td>
<td>0</td>
<td>+</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>-</td>
</tr>
</tbody>
</table>

Electrical Contact Points

One special EtherCAT feature is the option to route all communication through the internal I/O terminal bus as well. However, the superior performance often cited in connection with this feature is offset by the safety risk due to increased susceptibility for interference (contacts and EMC).
**Hot Plugging Capability**

POWERLINK, EtherNet/IP, and PROFINET give users hot plugging capability. Some restrictions apply for SERCOS III and EtherCAT due to the compulsory ring topology. In a physical ring topology, SERCOS III does allow for taking a single node off a network. In this event, the two neighboring nodes close the TX and RX lines. Nodes can then be reached from either side of the master. EtherCAT provides some hot plugging capability: In the EtherCAT Slave Controller, open ports are automatically closed if no link is detected. EtherCAT’s distributed clocks, however, requires re-synchronization, which may affect certain applications.

<table>
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<th>SERCOS III</th>
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<tbody>
<tr>
<td>Hot plugging</td>
<td>+</td>
<td>+</td>
<td>+</td>
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</tbody>
</table>

For technologies based on a logical ring (EtherCAT and SERCOS III), the limitations of the network topology also limit hot plugging capability. Hot pluggable modules can only be connected to one end of a daisy chain (SERCOS III) and distributed clocks require re-synchronization after node failure, which may impose restrictions on applications.

**Gigabit Readiness**

As EtherNet/IP and POWERLINK are entirely software-based technologies, these protocols can also be used with Gigabit hardware. EtherCAT can be scaled to Gigabit but requires an ASIC redesign. PROFINET IRT also requires some redesign of the hardware, which concerns switches in particular. FPGA solutions can be ported to Gigabit.

<table>
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<tr>
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<th>EtherCAT</th>
<th>SERCOS III</th>
</tr>
</thead>
<tbody>
<tr>
<td>Gigabit readiness</td>
<td>+</td>
<td>-</td>
<td>+</td>
<td>-</td>
<td>o</td>
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</table>

sercos International has stated that their IP core is basically Gigabit-ready.

**Support of International Standards**

The IEC 61158 international standard standardizes protocols (called “Types”) for use in industrial control systems. IEC 61784-2 standardizes communication profile families (called “CPF”). GB standards are National Chinese Standards written and issued by the Standardization Authority in China (SAC). They are valid across all industries and nationwide. GB/Z stands for national technical guidelines. These are primarily informative in nature and in no way binding. The highest authorized standardization level for communication technologies is GB/T. As a Chinese recommended industrial standard, GB/T must meet several requirements: It must be fully open technology, widely used and standard technology in the world. It must not be subject to any country or company.

<table>
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<tr>
<th>Criteria</th>
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<th>SERCOS III</th>
</tr>
</thead>
<tbody>
<tr>
<td>IEC 61158 Type 10</td>
<td>+</td>
<td>o</td>
<td>+</td>
<td>+</td>
<td>+</td>
</tr>
<tr>
<td>IEC 61158 Type 13</td>
<td>+</td>
<td>o</td>
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<tr>
<td>IEC 61158 Type 12</td>
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<td>o</td>
<td>+</td>
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<tr>
<td>IEC 61158 Type 19</td>
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<td>o</td>
<td>+</td>
<td>+</td>
<td>+</td>
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<tr>
<td>GB National Chinese Standard GB/Z 25105-2010</td>
<td>+</td>
<td>o</td>
<td>+</td>
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<td>+</td>
</tr>
<tr>
<td>GB National Chinese Standard GB/T 27960-2011</td>
<td>+</td>
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<td>+</td>
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<td>GB National Chinese Standard GB/Z 26157-2010</td>
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<td>o</td>
<td>+</td>
<td>+</td>
<td>+</td>
</tr>
</tbody>
</table>

**Products on the Market**

IRT products based on ERTEC technology are generally available on the market. However, the introduction of the DFP feature and the new generation of ASICs in conjunction with it (e.g. the Tiger Chip supplied by Phoenix) has raised doubts concerning the future compatibility of current IRT solutions.
The performance of the systems has been the subject of intense debate, which has focused on the theoretical cycle times achievable by Industrial Ethernet systems. The briefest possible cycle time in theory is calculated as follows:

<table>
<thead>
<tr>
<th>Number of bytes:</th>
<th>7</th>
<th>1</th>
<th>6</th>
<th>6</th>
<th>2</th>
<th>38 ... 1500</th>
<th>4</th>
</tr>
</thead>
<tbody>
<tr>
<td>Destination</td>
<td>1010...0101.011</td>
<td>Source</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Preamble</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Starting frame delimiter</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Destination MAC address</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Source MAC address</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Length (if &lt;1501*) / EtherType (if &gt;1535*)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Payload data</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Check sequence (cycling redundancy check)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

*Decimal values

<table>
<thead>
<tr>
<th></th>
<th>Number of bytes</th>
<th>Number of bits</th>
<th>Duration at 100 Mbit/s</th>
</tr>
</thead>
<tbody>
<tr>
<td>Minimum length</td>
<td>26 + 38 = 64</td>
<td>512</td>
<td>5.1 µs</td>
</tr>
<tr>
<td>Maximum length</td>
<td>26 + 1500 = 1526</td>
<td>12208</td>
<td>122 µs</td>
</tr>
</tbody>
</table>

Source: frame makeup as defined in IEEE 802.3
(The interframe gap of .96 µs must be added on top of the 5.1 µs cited above.)
Hence, if a master sends out a frame addressed to itself that does not pass through any other nodes, that frame will be available to the master again after 122 microseconds have elapsed (in the case of a single, maximum-length Ethernet frame).

In theory, it would be possible to process parts of a frame as soon as they are received. However, the CRC bytes that confirm the validity of the data received are last to arrive at the end of a frame. This scenario does not factor in delays affected by PHYs, cables, and Ethernet ports, times for internal data transfer in the master, etc. Moreover, once a signal leaves the master, the time it takes to travel along network lines (5 ns/m) and the processing time inside a slave have to be taken into account as well.

Prospective extensions of a system and possible future requirements need to be carefully considered for selecting either a centralized or a decentralized architecture. One advantage of the decentralized processing of various control loops is that it allows for adding nodes without any noticeable effect on the basic cycle time, i.e. no fundamental changes to the overall concept must be made. Moreover, additional functionality such as condition monitoring or integrated safety technology will have less impact on the control concept than in central architectures, which depend significantly on a low volume of data.

In order to select a solution that is viable for future use as well, wherever possible preference should be given to a decentralized handling of control loops for cycle times below 500 microseconds, especially in drive applications.

### Communication Architecture of the Systems

<table>
<thead>
<tr>
<th>Criteria</th>
<th>PROFINET RT</th>
<th>POWERLINK</th>
<th>EtherNet/IP</th>
<th>EtherCAT</th>
<th>SERCOS III</th>
</tr>
</thead>
<tbody>
<tr>
<td>Supports central control</td>
<td>+</td>
<td>+</td>
<td>+</td>
<td>+</td>
<td>+</td>
</tr>
<tr>
<td>Supports decentral control</td>
<td>+</td>
<td>+</td>
<td>+</td>
<td>-</td>
<td>0</td>
</tr>
</tbody>
</table>

### Direct Cross-Traffic

Direct cross-traffic provides crucial benefits particularly in case of very demanding real-time requirements: for fast drive controllers, axes can be synchronized easily and with extreme precision, since all position values can be distributed directly without having to go through a master. That results in lower network load and also ensures that data (e.g. actual angle positions of axes) is available to all relevant nodes within the current cycle. If data needs to pass through a master first, it is not only delayed by one cycle, but overall data traffic on the network is increased as well.

With POWERLINK and SERCOS III, direct cross-traffic is a feature even for modules that only have slave functionality, while EtherNet/IP requires a module with scanner functionality.
### Heavy Data Traffic

In applications involving a large volume of process data, the time required for passing through the nodes greatly impacts the overall cycle time. Data prioritization, on the other hand, enables lower cycle times. Systems that support prioritization mechanisms allow for reading high-priority data once every cycle and polling for data with a lower priority only every n-th cycle.

<table>
<thead>
<tr>
<th>Criteria</th>
<th>PROFINET RT</th>
<th>IRT</th>
<th>POWERLINK</th>
<th>EtherNet/IP</th>
<th>EtherCAT</th>
<th>SERCOS III</th>
</tr>
</thead>
<tbody>
<tr>
<td>Prioritization</td>
<td>+</td>
<td>+</td>
<td>+</td>
<td>-</td>
<td>+</td>
<td>+</td>
</tr>
</tbody>
</table>

For POWERLINK, EtherNet/IP, and PROFINET, variable cycle times have been firmly established in the protocols’ specifications. SERCOS III has only recently added this feature. For EtherCAT, solutions for this requirement can be implemented as part of a specific application.

### Network Load for Safety Communication

Safety over Ethernet is based on a cyclic exchange of protected data between safety nodes (emergency stop switches, drives with Safety controllers). The safeguard procedures in this process involve data duplication and wrapping data in safe "containers". This increases data rates on the network. Solutions using the summation frame method will see the frame count go up, whereas the single frame method will increase the volume of data in each of the frames that are due to be sent anyway. All in all, the theoretically superior performance of the summation frame method is neutralized.

### Actual Cycle Time

In solutions using the summation frame method, data must pass twice through each controller. If a signal has to go through many nodes, total transfer time will rise considerably as it makes its way. Raw performance data cited by the organizations supporting such solutions has to be adjusted to account for this effect. Another aspect to consider is that performance depends on implementation specifics, e.g. task classes, in the actual control systems used for an application.

<table>
<thead>
<tr>
<th>Criteria</th>
<th>PROFINET RT</th>
<th>IRT</th>
<th>POWERLINK</th>
<th>EtherNet/IP</th>
<th>EtherCAT</th>
<th>SERCOS III</th>
</tr>
</thead>
<tbody>
<tr>
<td>Performance</td>
<td>-</td>
<td>+</td>
<td>+</td>
<td>-</td>
<td>+</td>
<td>+</td>
</tr>
</tbody>
</table>

### Jitter

It is crucial for control quality on a network to ensure minimal jitter (clock deviation) and to determine signal delays very precisely. To this end, network nodes must be synchronized as precisely as possible. Competing Ethernet variants employ different mechanisms to achieve that goal. While EtherCAT uses the principle of distributed clocks solved by a proprietary algorithm within the ESC (EtherCAT Slave Controller), synchronization is accomplished via a simple sync signal (SoC) in POWERLINK networks.

<table>
<thead>
<tr>
<th>Criteria</th>
<th>PROFINET RT</th>
<th>IRT</th>
<th>POWERLINK</th>
<th>EtherNet/IP</th>
<th>EtherCAT</th>
<th>SERCOS III</th>
</tr>
</thead>
<tbody>
<tr>
<td>Jitter</td>
<td>O</td>
<td>+</td>
<td>+</td>
<td>O</td>
<td>+</td>
<td>+</td>
</tr>
</tbody>
</table>

EtherCAT, POWERLINK, and SERCOS III give users a system with almost no jitter (< 100 ns) at all times. On EtherNet/IP networks, jitter can be considerably reduced with special IEEE 1588 extensions in all components. Reduced jitter can also be achieved in PROFINET IRT applications.
Performance Contest

In practice, comparing system performance proves to be a difficult endeavor due to the specific characteristics of the various systems: EtherNet/IP and PROFINET RT are excluded from the start because these systems are only suitable for soft real-time requirements. PROFINET IRT poses problems due to the indispensable switches, which lead to a different application architecture that makes a direct comparison of measurements complicated. The values below were determined based on published calculation schemes.

Test scenarios were

1. a small machine comprising a master and 33 I/O modules (64 analog and 136 digital channels);
2. an I/O system with a master and twelve Ethernet slaves with 33 modules each (in total, 2000 digital and 500 analog channels were taken into account in this application);
3. a Motion Control network with 24 axes and one I/O station with 110 digital and 30 analog I/Os.

In practice, POWERLINK is faster than EtherCAT in most applications. EtherCAT is optimized for applications with only very low network traffic volume. In systems with a heavier data load, there is a disproportionate rise in cycle times in EtherCAT environments. Where decentralized architectures (e.g. for decentralized Motion control) are implemented, EtherCAT suffers greatly from the lack of direct cross-traffic (in both directions), which sharply reduces the performance that can theoretically be achieved. A direct I/O integration of EtherCAT also results in lower sampling rates (I/O system), since the time the signal takes to pass through the I/O has a direct impact on the cycle time within reach.

For POWERLINK and SERCOS III, there are no such effects. The publication by Prytz (2008) was used as a reference for the calculations concerning EtherCAT. Delays for signals passing through the EtherCAT ASIC were verified again by measurements. For POWERLINK, applications with actual products were set up for practical measurements, leaving no room for doubt and reconfirming the cited figures.

No tests and calculations were conducted for SERCOS III. However, SERCOS III can be expected to provide a performance level similar to POWERLINK, making it faster than EtherCAT in many applications.

<table>
<thead>
<tr>
<th>Scenario</th>
<th>EtherCAT</th>
<th>POWERLINK</th>
<th>Better</th>
</tr>
</thead>
<tbody>
<tr>
<td>Motion decentralized: EtherCAT</td>
<td></td>
<td>269.98</td>
<td>542.88</td>
</tr>
<tr>
<td>Motion decentralized: POWERLINK</td>
<td></td>
<td>363.48</td>
<td></td>
</tr>
<tr>
<td>I/O System: EtherCAT</td>
<td>325.25</td>
<td></td>
<td></td>
</tr>
<tr>
<td>I/O System: POWERLINK</td>
<td>271.44</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Motion centralized: EtherCAT</td>
<td></td>
<td>260.98</td>
<td></td>
</tr>
<tr>
<td>Motion centralized: POWERLINK</td>
<td></td>
<td>260.98</td>
<td></td>
</tr>
<tr>
<td>Small I/O: EtherCAT</td>
<td>81.21</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Small I/O: POWERLINK</td>
<td>53.4</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

Implementation costs include development expenses, license costs, and hardware costs. Code availability (program or VHDL in case of a hardware implementation) must be taken into consideration here as well.

**Master Implementation**

<table>
<thead>
<tr>
<th>Master designs</th>
<th>PROFINET</th>
<th>POWERLINK</th>
<th>EtherNet/IP</th>
<th>EtherCAT</th>
<th>SERCOS III</th>
</tr>
</thead>
<tbody>
<tr>
<td>Master access</td>
<td>-</td>
<td>+</td>
<td>-</td>
<td>o</td>
<td>+</td>
</tr>
<tr>
<td></td>
<td>no open source master available</td>
<td>openPOWERLINK (open source)</td>
<td>no open source master available</td>
<td>patent-protected ¹</td>
<td>common SERCOS III master API (open source)</td>
</tr>
<tr>
<td>Implementation costs</td>
<td>o</td>
<td>-</td>
<td>+</td>
<td>o</td>
<td>+</td>
</tr>
<tr>
<td></td>
<td>pricey software stack requires special hardware with coprocessor</td>
<td>runs on standard hardware</td>
<td>pricey software stack</td>
<td>runs on standard hardware</td>
<td>typically with coprocessor support</td>
</tr>
</tbody>
</table>

¹ No open source master, only sample code that does not warrant applicability.

*All protocols allow for a software implementation of the master on a standard Ethernet chip.*
**Costs for Potentially Required Network Components**

External devices  =  external switches or hubs

Internal multiports  =  ports that are directly integrated into the devices, primarily for daisy chain and ring topologies

<table>
<thead>
<tr>
<th>Network components costs</th>
<th>PROFINET RT</th>
<th>IRT</th>
<th>POWERLINK</th>
<th>EtherNet/IP</th>
<th>EtherCAT</th>
<th>SERCOS III</th>
</tr>
</thead>
<tbody>
<tr>
<td>External devices</td>
<td>+</td>
<td>0</td>
<td>+</td>
<td>0</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td></td>
<td>standard switch</td>
<td>special switch, IRT support required</td>
<td>standard hubs or switches</td>
<td>managed switch with complex functionality required (IGMP snooping, port mirroring, etc.)</td>
<td>special network components required 1</td>
<td>designated for future use of external infrastructure devices, but no such use at the time of writing</td>
</tr>
<tr>
<td>Internal multiports</td>
<td>0</td>
<td>0</td>
<td>+</td>
<td>0</td>
<td>+</td>
<td>+</td>
</tr>
<tr>
<td></td>
<td>integrated switch</td>
<td>Siemens ASIC required</td>
<td>standard hub</td>
<td>integrated switch, very complex</td>
<td>Beckhoff ASIC required 2</td>
<td>FPGA-based technology</td>
</tr>
</tbody>
</table>

1 With EtherCAT, special network components are required for star or tree topologies.

2 Beckhoff ET1100.
Slave Implementation

For EtherCAT, SERCOS III, and PROFINET IRT, bus protocol implementations into a slave require hardware solutions (ASICs or FPGAs). For POWERLINK, EtherNet/IP, and PROFINET RT, microcontroller-based software solutions are also feasible. Expenses for software solutions comprise license costs for the stack, possibly complemented by extra costs for more powerful and therefore more expensive controllers.

For hardware solutions, users may opt for either FPGA- or ASIC-based communication interfaces. In principle, FPGAs may also be used for software solutions.

An FPGA (Field-Programmable Gate Array) is an integrated circuit that hardware developers can configure themselves. It consists of programmable logic components, so-called logic blocks, and a hierarchy for the reconfigurable component circuitry. All logic functions ASICs are able to execute can be implemented with FPGAs as well. Functionality can be customized before commissioning. The one-off development costs for FPGAs are lower than that of ASICs. FPGAs comprise an attractive technology for Industrial Ethernet solutions primarily due to these lower expenses, their high performance, and multi-protocol capability, but also because they allow for using pre-assembled components to integrate Layer 2 functionality (hubs, switches). However, users need to be aware that the complexity of a protocol has an impact on the volume of code and, by extension, the required number of logic blocks. L2 functionality can also have a substantial bearing on this number. Switches need more blocks than hubs, and complex managed switches require an excessive number of logic blocks. POWERLINK is the least complicated real-time Ethernet solution. Moreover, since POWERLINK only resorts to hubs in its network layout, this protocol requires only a small number of logic blocks, and is suitable for small FPGAs.

EtherCAT and SERCOS III, on the other hand, are more demanding and therefore need many more logic blocks.

Node Connection Costs in Different Real-time Ethernet Environments

The connection costs per node below refer to the running expenses for the hardware. Potentially owed license costs for software stacks etc. have not been taken into account.
The figures in this diagram have been derived from feedback from various manufacturers with implementation experience covering different Industrial Ethernet solutions. Several figures have also been quoted by manufacturers in automation industry magazines. Costs for the PHY (2 × 1.1 $) have been factored in equal measure for all protocols. Connectors are excluded.

Cost estimates per node are made for an annual volume of 1000 units.

**PROFINET:**
The calculation reflects a solution with an ERTEC200 ASIC. Future implementations may also use devices equipped with a TPS1 chip developed by Phoenix Contact. In that case, costs are likely to drop to a level comparable to EtherCAT. POWERLINK’s price level will not be met.

**POWERLINK:**
The calculation applies to an FPGA-based solution. RAM and flash memory costs have been taken into account.

**EtherNet/IP:**
The EtherNet/IP figure applies to a typical FPGA solution.

**EtherCAT:**
The calculation is based on the least expensive EtherCAT ASIC solution with two Ethernet ports (ET1100). EtherCAT solutions for FPGAs are much more costly; the difference is most striking for synchronous solutions with real-time clocks.

**SERCOS III:**
The SERCOS III figure applies to a typical FPGA solution.

---

**Operating Costs**

Operating costs largely consist of maintenance and network administration expenses. Some technologies such as EtherNet/IP with CIP Sync and PROFINET IRT are highly complex and may therefore entail considerable network administration costs. Moreover, any use of managed switches requires network expertise. In many cases, a network engineer will be needed on location for commissioning and maintenance.

The synchronization technology used by a solution plays a key role for real-time communication. POWERLINK and SERCOS III ensure synchronization via a master-managed mechanism that is very precise and very rarely disturbed by faults. PROFINET IRT and EtherNet/IP with CIP Sync depend on an IEEE 1588 compliant synchronization mechanism. That results in significantly more complex network administration, especially if devices must be isolated because they trigger synchronization faults due to hardware or software failures.

Functions such as hot plugging – i.e. the option to swap devices on a live network – can also help greatly to bring down maintenance costs: replacement devices can be updated and configured without any impairment to the real-time function of the system as a whole.
Safety Functionality

Requirements toward safety in production environments have become considerably more demanding over the past decade. Introduction of the 2006/42/EC Machinery Directive by the European Union made machine and plant manufacturers focus their attention on this issue. They are required to design comprehensive solutions to ensure protection of workers against injuries and the machinery itself against damage while maintaining high levels of productivity.

The new standards led to the necessity for new machinery to undergo strict certification procedures and to elevated performance requirements for the safety components used. Supported by a multitude of new and innovative safety products, they also facilitated changes in the approach toward the conceptual design of safety solutions. No longer is an emergency stop immediately halting all parts of a machine the only safe reaction to violations of the machine’s safe boundaries. Smart safe reactions such as continued operation at a safe limited speed can in many cases deliver the required level of protection while providing better productivity by reducing the time to resuming full speed. In many instances, it enables a more direct interaction between worker and machine, particularly in teaching and adjustment scenarios.

Network Integrated rather than Hard Wired

Traditionally, safety equipment used to be hard wired with dedicated switching circuitry, often logic cast in pure hardware. Although with some effort it is theoretically possible to cover many cases using this method, more and more machine manufacturers have been recognizing the benefits of integrated safety. It is based on safe programmable control hardware and I/O modules using the existing field bus to exchange safety-related data.
At first glance, the older methods may appear less costly. Due to the lower purchasing costs of their hardware components, this may in many cases be true, but not if safety solutions are viewed in their entirety. Wherever the complexity of such systems goes beyond a single emergency stop button, network-integrated safety systems have become the preferred choice. They lower the number of components as well as required cabling and provide more flexibility of safe logic design by replacing hard wiring with configuration and parameter setting. Also, error diagnostics are greatly simplified. Combined with centralized data storage, this results in faster recovery. Maximum availability of plants and machines is provided by network-integrated safety technology through:

- Safety sensors directly attached to the network
- Direct read-out of component information
- Simplified maintenance due to automated component parameter setting across the network
- Safer operating mode switching due to parameter setting during runtime
- Decreased response time, as latency induced by relays is eliminated
- Modular design supported by network structure and safe software
- Increased availability as a result of comprehensive diagnosis
- Reduction of component count and wiring
- Greater variety of safety functions
  (safe operating stop, safely limited speed...)

**How It Works**

Safety applications based on certified software are programmed using function blocks such as counters, timers or speed monitors. Running on dedicated safety controllers, this replaces the traditionally hard-wired safety circuitry. Implementation of the safety application in software reduces the number of both safety components and standard I/O modules. Along with the replacement of discrete cabling by safety data transfer via the existing network connections, this greatly minimizes both costs and complexity of safety installations. Due to the use of existing network connections, varying machine layouts and options do not require dedicated safety connections. This also improves flexibility and freedom of safety application design as well as modifications of existing plants and machines during their lifecycle. Also, diagnostic signals can be transferred without any additional hardware. All in all, using integrated safety speeds up engineering and substantially shortens time to market.

The Black Channel Principle

Safety Field Networks
Safety-oriented field buses simplify placement of components within a plant or machine. In most cases, two cables, one for power and one for communication, are all that is required. Sensors can be attached directly to the safety network. They do not require separate cables for the return of diagnostic signals. This results in a reduction of required hardware components.

Using the Black Channel Principle, safety-relevant data as well as diagnostic information is exchanged via the existing network connections. This enables faster responses. Sensors receive their parameter setting and configuration via the network. This allows the download of optimized parameter settings to the sensors in case of changes to the mode of operation and also eliminates the need to adjust parameters at the device itself in case of component replacement after failure. All this adds to a maximized productivity and reduced down time.

Safety Data Transport via Standard Bus or Network Lines
The Black Channel Principle allows transmission of failsafe and standard data via the same network or bus line. Independent of the regular data transport mechanism used on that line, safety components can transmit data using an isolated safe protocol tunneling the underlying network channel. As safe fieldbuses are pure application protocols without physical characteristics of their own, available bandwidth and cycle times depend on the data transport protocol used. Possible transmission errors are known and listed in the relevant standards IEC 61784-3 and IEC 61508. Their prevention needs to be implemented as a crucial part of the safety data transmission protocols. The required quality of transmission error detection depends on the safety level that needs to be achieved.

Black-Channel mechanism
CIP Safety

The “CIP Safety” protocol was specified for safety data transmission via EtherNet/IP or DeviceNet. Using the pre-existing CIP (Common Industrial Protocol) Services as its foundation, the CIP Safety protocol makes use of the producer-consumer mechanism for the exchange of data between safe nodes. In this context, a consumer is designated “originator” and a producer is called “target”. Safe time synchronization between producers and consumers relies on chronological monitoring. Synchronicity between all nodes throughout the network provided, the time of origination of safe messages can be determined using a time stamp. These methods guarantee that processed data is still up to date. For the transfer of the safe data, “Safety Validator Objects” are used. They organize and guarantee the integrity of messages in a CIP Safety network. These objects also constitute the bridge between the safety communication and the field bus or network used. For data transmission, the protocol provides single transfer or multicast connections. Their use depends on the capability of the channel used to support either of these connections.

For CRC (cyclic redundancy check) calculation, the CIP Safety protocol makes use of five different formats, ranging from 8 bit to 32 bit CRC. These depend on whether data size is one or two Bytes or between three and 254 bytes and of the data range to be covered by checksum calculation. A “Unique Node Identifier” (UNID) is used for the unique identification of the safe nodes. It is a combination of a network ID and the node address, which is equivalent to the MAC address. It can be set either manually using DIP switches or via software configuration. During the ramp-up phase, the originator checks the presence of the configured UNIDs in the network. Further parameters such as timeout delays, ping intervals or the maximum number of nodes are configured using a Safety Configuration Tool (SNCT).
PROFIsafe uses the “Master-Slave” mechanism for transmission of safety telegrams. The master, typically called the “F-Host”, cyclically exchanges safety-relevant data with all its configured slaves called “F-Devices”. Each F-Device has an F-Driver organizing the co-ordination of safe messages called “Safety PDUs” (Protocol Data Unit) between the F-Host and F-Slave. CRC calculation of the PDUs depends on the message size to be transferred, the distinction being between “slim PDU” up to 12 bytes and “long PDU” up to 123 bytes. CRC 24 is used for slim PDU calculation, while for long PDUs, CRC 32 is used. As a means for message recipients to determine whether telegrams arrive in the right sequence, PROFIsafe uses consecutive numbers for the safety telegrams. Additionally, monitoring of the tolerance time (F-Watchdog Time) that is reset upon receipt of a telegram ensures that always the currently valid telegrams are read. The so-called F-Parameters (PROFIsafe parameters) supply a unique identifier between F-Host and F-Device.

Although the addresses (Unique Codename) are automatically passed on to the F-Devices, the target addresses need to be adjusted directly at the device via DIP switches. The F-Devices receive their configuration through transfer of the F-Parameter via “GSD” (General Station Description) and of the I-Parameter (individual F-Device Parameter). These parameters are managed within the iPar server, from where they can be transferred to a PROFIsafe Device using standardized interfaces. Usually, the iPar Server comes integrated in a “CPD-Tool” (collaborative product design) engineering tool. For product designers, this means that for the ability to completely configure an F-Device, a GSD file needs to be created and an interface to the CPD-Tool must be provided for each product.
**openSAFETY**

openSAFETY was designed aiming at transmission of safety relevant data over any field bus or network. It can be used on all field buses, Ethernet-based or not.

For the transmission of safety data, the producer-consumer model is used. The advantage of this model is that all consumers in an openSAFETY network can receive and subsequently process the messages sent by the producer. Each openSAFETY node has a unique UDID (openSAFETY Unique Device Identification) number. This is a combination of the MAC address and the manufacturer’s device number. During the booting process, the Safety Network Management (SNMT) checks the device type and the UDID, so that it automatically detects replaced devices. In such a case, the required parameters are automatically transferred to the Safety Nodes (SN). The SNMT can optionally be integrated with the Safety Configuration Manager (SCM). In analogy to other communication protocols, the SCM can be viewed as an openSAFETY master using services to manage the network.

The openSAFETY Object Dictionary (SOD) manages all parameters, which are then transferred to the safety nodes using Safety Service Data Objects (SSDO). Upon completion of Node configuration and the booting phase, the cyclic data transfer between producer and Consumer commences. For the transfer of safety-critical process data, this uses Safety Process Data Objects (SPDO). The openSAFETY frame consists of two sub frames. It can transport a maximum of 254 bytes of safety data, using CRC 8 for payloads from 1 to 8 bytes and CRC 16 for payloads from 9 to 254 bytes.

With openSAFETY, very large networks can be created. For each openSAFETY Domain (SD), up to 1023 safe nodes can be connected. As they are addressed by the SCM, no additional hardware switches are required. The maximum total configuration of an openSAFETY network has 1023 openSAFETY Domains with a total of more than a Million safe nodes. Communication between the individual Domains is performed by the openSAFETY Domain Gateway (SDG).
FSoE

Fail Safe over EtherCAT (FSoE) is a transmission scheme for safety data over EtherCAT using an FSoE master and FSoE slaves. In each FSoE cycle, the master sends its Safety PDU (Protocol Data Unit) to the slave, concurrently starting a watchdog timer. The slave verifies and calculates the data received prior to returning it to the master. In this case, the slave also starts a watchdog timer. The master receives and processes the data as described for the slave, stopping the watchdog timer. Only when this cycle is completed, the master generates a new Safety PDU. Due to this mechanism, safe communication always depends on the hardware and topology used.

The address relation between a master and a slave is called “FSoE-Connection”. It is characterized by a unique Connection ID. The 16-bit Connection-ID is transferred by the master to the individual slaves. Users need to take measures to ensure providing each slave with a unique ID. For correct identification of the ramp-up sequence, both the master and the slave generate a “Sequence Number” ranging from 0 to 65535 for every message. This ensures that only currently valid messages are processed. Addressing of the individual devices requires designation of unique Node numbers by hardware setting using DIP switches. Each FSoE master includes an “FSoE master Handler”, which communicates with a slave through an “FSoE slave Handler”. Optionally, an additional “FSoE slave handler” that can be implemented in the master allows communication between different masters within a network. For safeguarding the PDUs to transfer, for every 2 bytes of Safety Data, a CRC 16 is used. This implies that for a 10 Byte transfer, a CRC 16 is applied five times.

Parameter setting as such is not specified. The parameterization process needs to be part of the user-programmed application software. While the FSoE specification does detail the required parameters, users need to ensure for the individual FSoE slaves to receive their correct parameters.
Generally speaking, all the various integrated safety technologies equally fulfill safety requirements. They are all based on the “Black Channel” principle, listed in the IEC 61784-3 standard and certified up to SIL 3. Hidden behind the raw safety aspects, however, are relevant criteria that define whether a technology will be adopted by component manufacturers or end customers. The distinguishing differences are the ease of integration of the technologies in the application serving the problem-solving purpose at hand.

<table>
<thead>
<tr>
<th>Certifications</th>
<th>Criteria</th>
<th>CIP Safety</th>
<th>PROFIsafe</th>
<th>openSAFETY</th>
<th>FSoE</th>
</tr>
</thead>
<tbody>
<tr>
<td>Black-Channel based</td>
<td>+</td>
<td>+</td>
<td>+</td>
<td>+</td>
<td></td>
</tr>
<tr>
<td>IEC 61784-3</td>
<td>+</td>
<td>+</td>
<td>+</td>
<td>+</td>
<td></td>
</tr>
<tr>
<td>Certification body</td>
<td>TÜV Rheinland</td>
<td>TÜV Süd</td>
<td>TÜV Süd</td>
<td>TÜV Süd</td>
<td></td>
</tr>
</tbody>
</table>

openSAFETY technology is certified up to SIL 3. Though it has not been qualified yet, the core principle of this technology, including probability of failure on demand (PFD), is ready for SIL 4.
Device Implementation

<table>
<thead>
<tr>
<th>Criteria</th>
<th>CIP Safety</th>
<th>PROFIsafe</th>
<th>openSAFETY</th>
<th>FSoE</th>
</tr>
</thead>
<tbody>
<tr>
<td>Legal limitation</td>
<td>+</td>
<td>–</td>
<td>+</td>
<td>–</td>
</tr>
<tr>
<td>Investment security</td>
<td>o</td>
<td>o</td>
<td>+</td>
<td>o</td>
</tr>
<tr>
<td>Time-To-Market</td>
<td>+</td>
<td>+</td>
<td>+</td>
<td>o</td>
</tr>
<tr>
<td>Implementation</td>
<td>–</td>
<td>o</td>
<td>+</td>
<td>+</td>
</tr>
<tr>
<td>Current market share</td>
<td>o</td>
<td>+</td>
<td>o</td>
<td>–</td>
</tr>
<tr>
<td>certified stack available</td>
<td>+</td>
<td>+</td>
<td>+</td>
<td>–</td>
</tr>
</tbody>
</table>

For device manufacturers, independence and implementation costs are the most significant considerations. For implementation, all costs for license fees, software stack, conformance test and certification were taken into account in the comparison. So was the complexity of each technology and its impact on the required resources and costs for implementation.

ProfiSAFE and FSoE are limited to the protocols of their user organizations. This could lead to the requirement to implement several safety protocols if equipment using different automation systems and fieldbuses is combined. CIP Safety requires the implementation of a dedicated CIP Abstraction Layer within the Black Channel, thus increasing engineering efforts. According to our researches a FSoE slave is under development, but currently there is no certified FSoE slave stack available on the market. This may pose a potential risk for safety technology implementations on the device level.

Integration

<table>
<thead>
<tr>
<th>Criteria</th>
<th>CIP Safety</th>
<th>PROFIsafe</th>
<th>openSAFETY</th>
<th>FSoE</th>
</tr>
</thead>
<tbody>
<tr>
<td>Stack compatibility</td>
<td>o</td>
<td>o</td>
<td>+</td>
<td>–</td>
</tr>
<tr>
<td>Performance</td>
<td>o</td>
<td>o</td>
<td>+</td>
<td>o</td>
</tr>
<tr>
<td>Addressing</td>
<td>+</td>
<td>–</td>
<td>+</td>
<td>–</td>
</tr>
<tr>
<td>Safe Reaction Time</td>
<td>o</td>
<td>o</td>
<td>+</td>
<td>o</td>
</tr>
</tbody>
</table>

To ensure compatibility between safety products from different manufacturers, compatibility of all stacks on the market is essential.

Open source strategy of openSAFETY guarantees stack compatibility.

In a safety network, all nodes must have unique IDs. To avoid parameter setting errors, addressing should be automated. The PROFIsafe and FSoE protocols, however, require manual address setting for each safety device using DIP switches. This makes installation of safety devices in the control cabinet more complicated. Human error, particularly in maintenance scenarios, could result in faulty parameter setting. It is also very difficult to create modular machine concepts using hardware switches, as this form of addressing is always rigid and components cannot be configured automatically.

Following the producer consumer principle, openSAFETY supports direct cross communication, which results in singularly fast reactions. Routing all safety messages via the master, as in PROFIsafe and FSoE, extends cycle times. Consequently, valuable time for safe reactions is lost.

CIP Safety requires originator functions to support cross-traffic, thus a cross-traffic between slaves (targets) is not possible.

The openSAFETY stack is currently the only open source software for safety communication. Technically as well as from a legal point of view, openSAFETY is entirely technology-independent.
Performance

Since Safety protocols are application protocols, a safety network’s performance depends on the underlying data transfer protocol. The base protocol selection determines the available bandwidth and the cycle times, but also functional features such as hot plugging capability or data communication via cross-traffic. Cross-traffic plays a crucial role in the performance of safety-oriented systems. In networks supporting cross-traffic, safety nodes can transmit signals directly to each other without routing them through a master. This provides for optimized reaction times in hazardous situations. On a network not supporting cross-traffic, safety nodes send their signals to a fieldbus master node, which relays it to the network’s safety master for acknowledgement. It is then handed back to the fieldbus master node which forwards it to the receiving safety node. Compared to direct data transfer via cross-traffic, this process causes four times the signal delay – valuable reaction time elapses. Since the emergency stopping distance of an axis increases with the square of the fault response time and negative acceleration, quadrupling the signal transfer time will result in a 16-fold extension of the emergency stopping distance.

<table>
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<th>FSoE</th>
</tr>
</thead>
<tbody>
<tr>
<td>CRC range</td>
<td>8-32 bits</td>
<td>24-32 bits</td>
<td>8-16 bits</td>
<td>16 bits</td>
</tr>
<tr>
<td>Required CRC computations for 20 bytes net data</td>
<td>2</td>
<td>1</td>
<td>2</td>
<td>10</td>
</tr>
<tr>
<td>Number of different CRC</td>
<td>5</td>
<td>2</td>
<td>2</td>
<td>1</td>
</tr>
</tbody>
</table>

The required number of different checksums increases implementation complexity, resulting in higher development costs. Additionally, computation of multiple CRCs may result in significantly slower reaction to safety violations.

Example for shorter signal transfer times due to cross-traffic: Cross-traffic enables safety nodes to directly communicate with each other (left), whereas signal paths are quadrupled in a system that does not support cross-traffic (right).